

CLAIMS

1. A method of transferring signals from a plurality of individual sensing elements provided on a first integrated circuit to a processing means provided on a second integrated circuit comprising the steps of sequentially sampling the output of a number of sensing elements in a predetermined sequence to create a first signal, modulating the amplitude of a constant frequency signal to create a second signal, transmitting said second signal from said first integrated circuit to said second integrated circuit, demodulating said second signal to regenerate said first signal and passing said regenerated first signal to said processing means.
2. A sensing device comprising an array of individual sensing elements provided on a first integrated circuit and processing means for the output of said array of said sensing elements provided on a second integrated circuit, said circuits being linked by a single conducting connection, said first integrated circuit comprising in addition to said sensing elements, sampling means for sequentially sampling the output of said sensing elements in a predetermined order to generate a first signal, signal generator means for generating a carrier signal of a constant known frequency, modulation means for modulating said carrier signal with said first signal to generate a second signal, and transmission means for transmitting said second signal to the second integrated circuit, said second integrated circuit incorporating means for receiving said second signal, means for demodulating said second signal to regenerate said first signal and means for processing said regenerated first signal.
3. A sensing device according to Claim 2 wherein the outputs of a first group of individual sensing elements are sampled and are then used to modulate a carrier signal of constant known frequency and, the output of a second group of individual sensing elements is simultaneously sampled and used to modulate a carrier signal of a different constant known frequency, both modulated signals being simultaneously transmitted to said second

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integrated circuit and simultaneously demodulated after arriving at said second integrated circuit.

4. A sensing device according to Claim 2 or Claim 3 wherein outputs of several such groups of individual sensing elements are simultaneously sampled, modulated, transmitted and demodulated.
5. A sensing device according to Claim 4 wherein the groups of sensing elements correspond to individual rows or columns in the sensing array, the sampling sequence within the group starting with the sensor at one end of the said row or column and finishing with the sensor at the opposite end of said row or column.
6. A sensing device according to Claim 5 wherein each row or column is provided with dedicated modulating means and the modulated signals are subsequently combined by a suitable combining means.
7. A sensing device according to any one of Claims 2 to 6 wherein the  
15 sampling process is repeated instantly.
8. A sensing device according to any one of Claims 2 to 6 wherein there is a predetermined delay between the successive sampling sequences.
9. A sensing device according to any one of Claims 5 to 8 wherein each sensing element in a row or column is connected to a row or column output conductor by a switch.
10. A sensing device according to any one of Claims 2 to 9 wherein sequential sampling of the outputs of individual sensing elements is carried out by sequentially connecting each sensing element to the row or column output conductor by closing each switch in turn.
11. A sensing device according to Claim 9 or Claim 10 in which each sensor generates a differential output, each said sensing element being connected by a pair of switches to a pair of output conductors, these switches being closed in turn.

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12. A sensing device according to any one of Claims 2 to 11 wherein said second signal undergoes analogue to digital conversion and is subsequently demodulated as a digital process.
13. A sensing device according to Claim 12 wherein signals resulting from a digital demodulation process are stored in registers for further image processing.
14. A sensing device according to Claim 12 or Claim 13 in which digital processing is carried out by a microprocessor.
15. A sensing device according to any one of Claims 5 to 13 wherein each carrier signal for each row or column or group of the individual sensing elements has a different frequency.
16. A sensing device according to Claim 15 wherein carrier frequencies are determined such that any odd harmonics that may be generated during the modulation process using one carrier frequency are at frequencies that do not fall close to other carrier frequencies.
17. A sensing device according to Claim 16 wherein carrier frequencies are produced by integer division from a single clock frequency signal.
18. A sensing device according to Claim 17 wherein a suitable clock frequency is 1 Megahertz and a suitable integer division ratios are one of 18, 20, 22, 25, 28, 33, 40 and 50.
19. A sensing device according to Claim 14 wherein a signal clock signal generator or synchronisation signal generator is connected to both integrated circuits.
20. A sensing device according to any preceding claim in which said sensing elements are IR (infrared) sensing elements.

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